**Questa Sim Execution Flow:**

**//Compile**

usb\_fl.sv //added to project, compiled

+incdir+C:/Users/sreeni/Desktop/USB\_Presentation/USB\_FINAL/usb\_20/design/rtl

+incdir+C:/Users/sreeni/Desktop/USB\_Presentation/USB\_FINAL/uvm-1.1b/src

**//Elaborate**

vsim -c work.top -novopt -sv\_lib D:/INSTALLATIONS/questa\_sim\_10.2b/uvm-1.1b/win32/uvm\_dpi +UVM\_TESTNAME=usb\_basic\_test

//Add log

add log -r sim:/top/\*

run -all

**//Code coverage**

vopt +cover=bcesxf top -o top\_c

vsim -coverage top\_c

coverage save tc1.ucdb

add log -r sim:/top/\*

run –all

coverage save tc2.ucdb

run –all

vcover merge tcF.ucdb tc1.ucdb tc2.ucdb

vcover report -html -htmldir reports –verbose tcF.ucdb

vcover report tcF.ucdb

